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/*
** adc.c - Sample code for HyperpanelOS ===== **
**
** This simple code is located into the application container, it is run **
** by the VMK sub-operating system. On the other hand, the I/O container **
** runs all the drivers that are VMIO finite state machines. **
**
** The goal of this small app is to use a ADS1115 ADC Converter **
** ===== **
*/

/* Documentation for I2C devices -----
Product used :

Ben-Gi Mini ADS1115 Module 4 channels 16 bits I2C ADC
www.amazon.fr/gp/product/B07PK1Z5H1
datasheet-ads1115.pdf - Texas Instruments
16-bit Analog-to-Digital Converter

The ADS1115 is a ultra small low power 16bit analog to digital converter. For
this simple demonstration app, the ADS1115 controller is used on the Ben-gi
Mini ADS1115 module with I2C interface. Main characteristics are:

- Analog to digital conversion module equipped with the high-precision
analog-to-digital conversion chip.

- This chip includes an internal reference voltage source and can provide 4
separate inputs or 2 differential inputs.

- Acquisition voltage input pin, default range: -6.44V ~ + 6.144V, when used
as a differential ground input voltage should be positive.

- Amplifier with internal programmable gain for changing the input range of
the acquisition voltage.

- Data can be transmitted via the I2C interface and the single chip

I2C Interface :

- I2C address are 0x90 (write) and 0x9 (read).

- The ADS1115 have a configuration register. The app write de configuration
in the initialisation step, and read the configuration register to check that
the write operation is ok. Configuration register is set as follow (cf.
datasheet page 18/19/20).

bit 15      Operational status                0      (default)
bit 14-12   Multiplexer config - INp=0 INn=1  000    (default)
bit 11-9    Gain amplifier FS= +/- 6.144V    000
bit 8       Device operating mode - Continuous 0
bit 7-5     Data rate - 850 sps              111
bit 4       Comparator mode - Traditional    0      (default)
bit 3       Comparator polarity - Active low  0      (default)
bit 2       Nn-latching comparator          0      (default)
bit 1-0     Disable comparator                11     (default)

                                0000000011100011 = 0x00E3

*/
/* Include files and external reference -----*/

#include <hypos.h>                // Hyperpanel OS basic interfaces.
#include <drv_asy.h>              // Prototype of "asy_write()".

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#include <drv_i2c.h>                                // Prototype of "i2c_*()".

/* Internal defines of this module -----*/
#define TICK          1000      // Code for tick event.

#define ADC_AD_W      0x90      // I2C dev address - ADC (write)
#define ADC_AD_R      0x91      // I2C dev address - ADC (read)

#define CONVERSION_REG 0x00      // I2C register - Converted value
#define CONFIG_REG     0x01      // I2C register - Configuration

#define INIT          0        // I2C command - ADC initialisation

/* Internal global variables of this module -----*/
static int    idto          ; // Timer identifier.

/* INIT command .....*/
static const char init[]    = // I2C command - ADC initialisation
{
/*   Length-1, Address, Control byte, Data byte          */
    3, ADC_AD_W , CONFIG_REG , // Write the configuration register
    0x00, 0xE3, // 2 bytes value.
    0, 0      , 0      , 0      , // End of command set
} ; //

static char *command[] = // I2C commands table
{
    (char*)&init[0]      , // I2C command - INIT
    (char*)0              , // End of list
} ; //

/* Prototypes -----*/
static int  loop_app_task(void*) ; // Prototype
static int  wait_evt(void)      ; // Prototype
static void set_command(int)    ; // Prototype

/* Beginning of the code -----
loop_app_tsk      Application entry point
*/

/* Procedure loop_app_tsk -----
Purpose : This is our task main loop.
*/

int loop_app_tsk (void *param)
{
    int          ev = TICK          ; // Our event
    char         mess[16]          ; // Message to be sent on ASY0
    unsigned char frame[16]        ; // I2C read frame
    int          ret = 0           ; // Return procedure code

/* Step 1 - Start a timer that will send an event every second .....*/
    set_tto(CLOCK      , // Timer mode: clock
            100        , // Duration in milliseconds

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        TICK , 0 , // Event code and reserve field
        &idto ); // Timer identifier

/* Step 2 - ADC initialisation .....*/

asy_write(0,(unsigned char*)"Init ... ",9);

set_command(INIT) ; // ADC initialization

asy_write(0,(unsigned char*)"done\r\n",6);

ret = i2c_read(0,ADC_AD_R , // Just for checking, read the register
              frame,2,CONFIG_REG); // we have just write.

hsprintf(mess,
         "Configuration register 0x%02x%02x (%d)\r\n",
         frame[0],frame[1],ret);

asy_write(0 , // Write on ASY0 the value of the
          (unsigned char*)mess , // configuration register.
          strlen(mess) ); // Count of bytes to be sent

/* Step 3 - Main loop .....*/

wait_ev : // Beginning of loop label

if ( ev == TICK ) // If the event is the tick event
{ //
  frame[0]=0 ; // Reset frame.
  frame[1]=0 ; // Reset frame.

  ret=i2c_read(0,ADC_AD_R , // Read the current value get from
              frame,2 , // the ADC.
              CONVERSION_REG) ; //

  hsprintf(mess , // Message with the 16-bit value.

           "Conversion register %05d (%d)\r\n",
           (frame[0]<<8)+frame[1],ret);

  asy_write(0 , // Write on ASY0
            (unsigned char*)mess , // the "mess" message
            strlen(mess) ); // Count of bytes to be sent
}

ev = wait_evt() ; // Unschedule until an event is received
goto wait_ev ; // Wait for the next event

return 0 ; // Return code of the procedure
}

/* Procedure wait_evt -----*/
/* Purpose : Unschedule until the next event is received, whatever it is.
*/

static int wait_evt (void)
{
  int waitlist[1][3] ; // Parameter of "waitevt_task"
  int ret ; // Return code for "waiyevt_task"

/*****
* Step 1 : Build a list with one WAIT_CODEINT entry that will accept all *
* ----- the event codes ranging from 0 to 20000. Then call *
* "waitevt_task", we will be unscheduled until the next event will *
* be received *
*****/

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waitlist[0][0] = WAIT_CODEINT ; // All events with
waitlist[0][1] = 0 ; // a code between 0
waitlist[0][2] = 20000 ; // and 20000

waitevt_task(waitlist , // Address of waiting list
              1 , // Size of "waitlist[]"
              0 , // maximum waiting time = no
              0 , // Do not purge previous events
              &ret ) ; // Return code

/*****
 * Step 2 : Here we are scheduled again. The VMK has written into its
 * ----- global variable "task_evt" a copy of the event that has
 * scheduled us again.
 *****/

return task_evt.code ; // Return event code
}

/* Procedure set_command -----*/
/* Purpose : Send a set of commands to I2C devices.
*/

static void set_command(int cmd)
{
    i2c_write(
        0 , // Controller number
        -1 , // Target I2C write address
        (unsigned char*)command[cmd], // Command
        0 ); // List of option flags
}

```